



## MOSFETs Silicon 100V N-Channel MOS

**■ Applications**

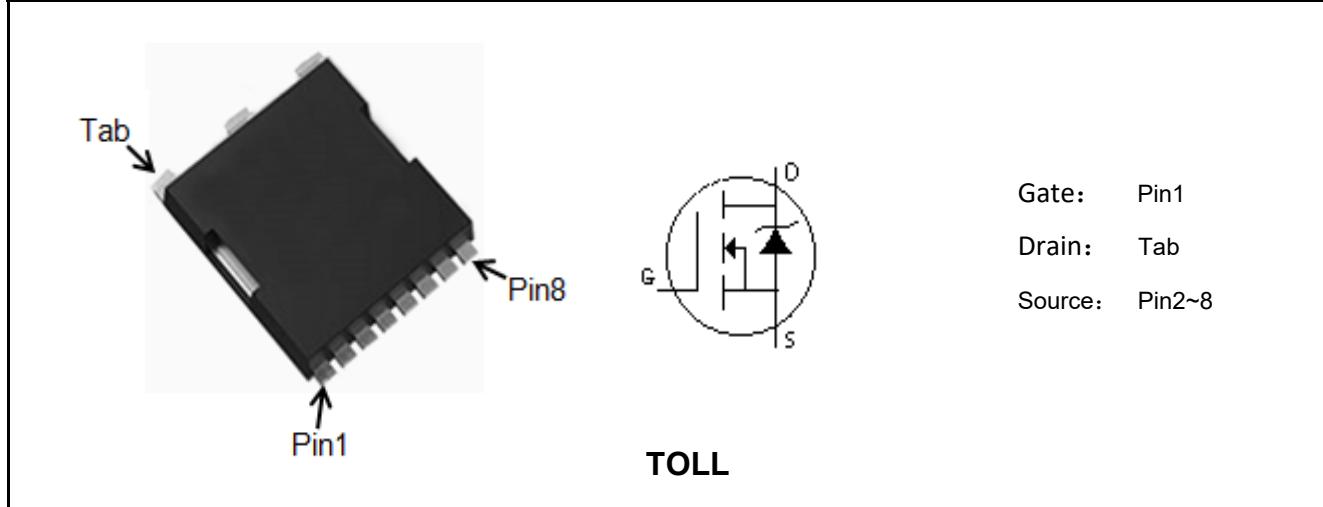
- Synchronous Rectification
- Industrial and Motor Drive
- DC/DC and AC/DC Converters
- Power TOLLS
- BMS

**■ Features**

- High-Speed Switching
- Low  $R_{DS(ON)}$
- Low Gate Charge
- RoHS and Halogen-Free Compliant
- 100% UIS and RG Tested

**■ Product Summary**

$V_{DS}$	100	V
$I_D$	470	A
$R_{DS(ON)} \text{ ,Typ@10V}$	1.1	$\text{m}\Omega$
$Q_g$	242	nC



Marking	Package	Packaging	Min. package quantity
MT1D3R100SH	TOLL	Tape & Reel	2000





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MT1D3R100SH

## ■ Absolute Maximum Ratings (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current T <sub>c</sub> =25°C (Note 1)	I <sub>D</sub>	470	A
Continuous Drain Current T <sub>c</sub> =100°C (Note 1)		330	A
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	1650	A
Total Dissipation	P <sub>D</sub>	577	W
Junction Temperature	T <sub>j</sub>	175	°C
Storage Temperature	T <sub>stg</sub>	-55-175	°C
Single Pulse Avalanche Energy (Note 2)	E <sub>AS</sub>	1930	mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

## ■ Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	R <sub>θJC</sub>	0.26	°C/W
Maximum Junction-to-Ambient	R <sub>θJA</sub>	40	°C/W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V<sub>DD</sub>=50V, T<sub>ch</sub>= 25°C(initial), L=0.5mH, R<sub>g</sub>=25Ω.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.





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## ■ Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100	-	-	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.0	2.8	4.0	V
Drain-Source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	1.1	1.3	mΩ
		T <sub>j</sub> =125°C	-	1.75	-	
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =35V, V <sub>GS</sub> =0V, f=1.0MHz	-	16200	-	pF
Output Capacitance	C <sub>oss</sub>		-	2500	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	170	-	pF
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1.0MHz	-	0.95	-	Ω
<b>Switching Paramters</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =50V, I <sub>D</sub> =100A, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω	-	31	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	78	-	ns
Turn-Off Delay Time	t <sub>d(off)</sub>		-	95	-	ns
Turn-Off Fall Time	t <sub>f</sub>		-	35	-	ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	242	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	51	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	72	-	nC
<b>Source-Drain Characteristics</b>						
Diode Forward Voltage	V <sub>sd</sub>	V <sub>GS</sub> =0V, I <sub>F</sub> =10A	-	0.75	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>R</sub> =50V, I <sub>F</sub> =20A, di/dt=100A/us	-	115	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	315	-	nC

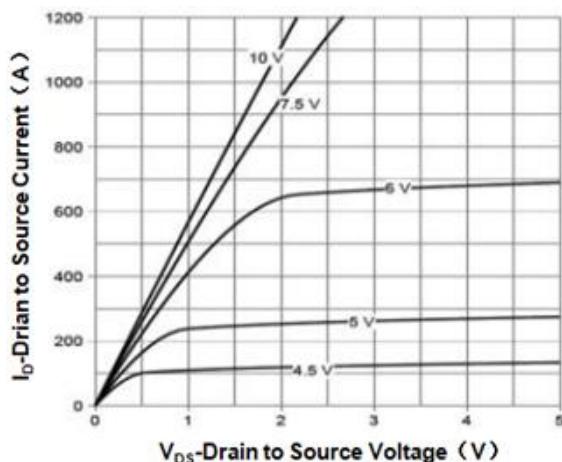




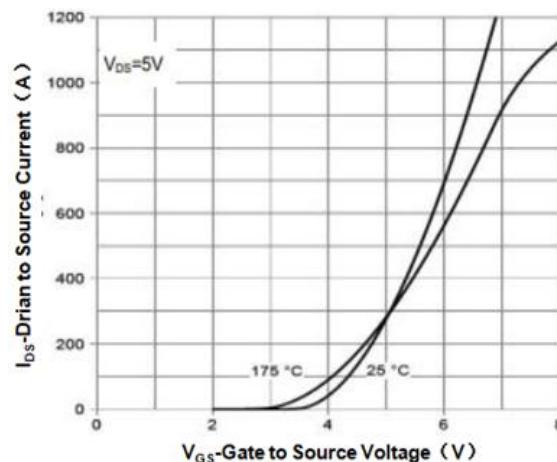
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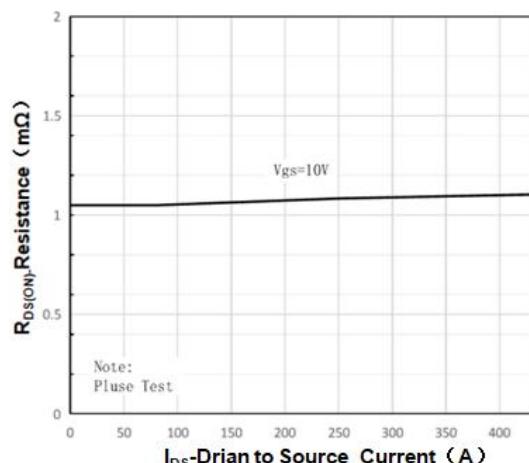
## ■ Characteristics Curves



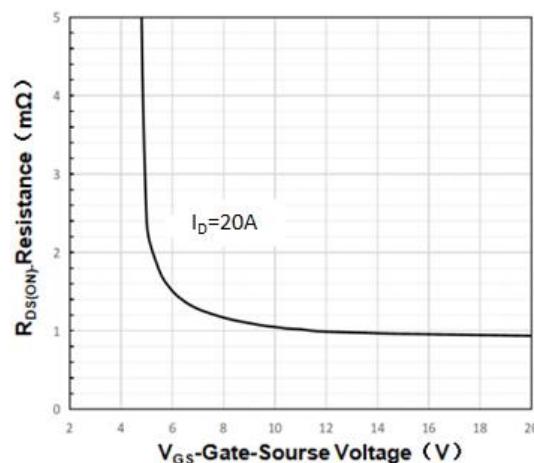
Output Characteristics



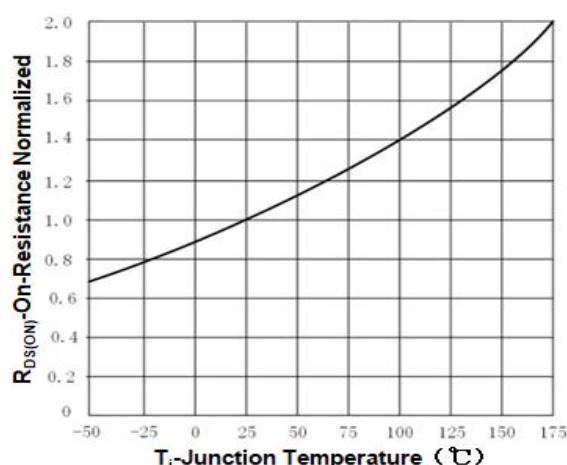
Transfer Characteristics



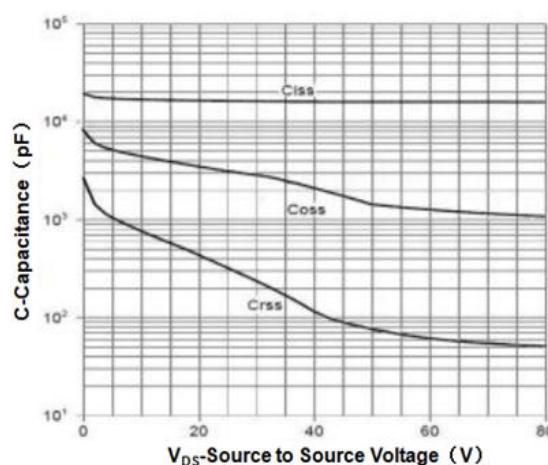
On Resistance Vs Drain Current



On Resistance Vs Gate Source Voltage



Rdson-JunctionTemperature



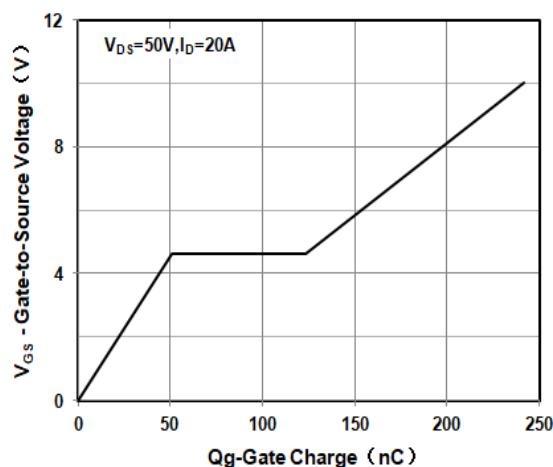
Capacitance



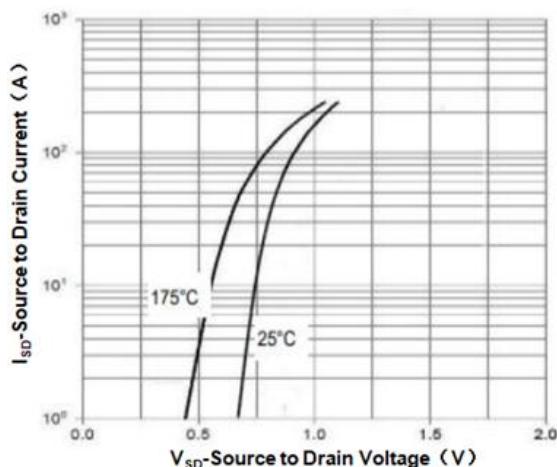


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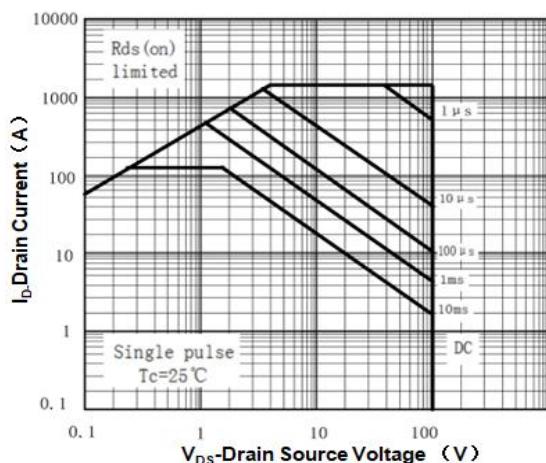
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Gate Charge Waveform



Source-Drain Diode Forward Voltage



Maximum Safe Operating Area

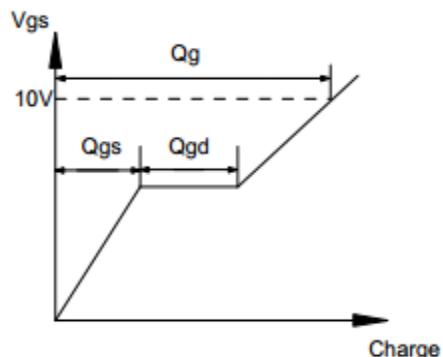
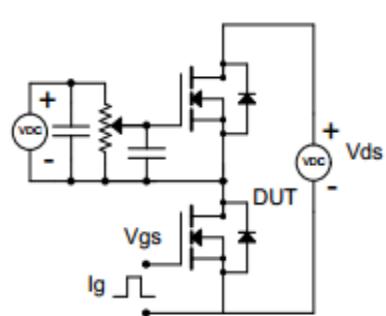
Note : The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



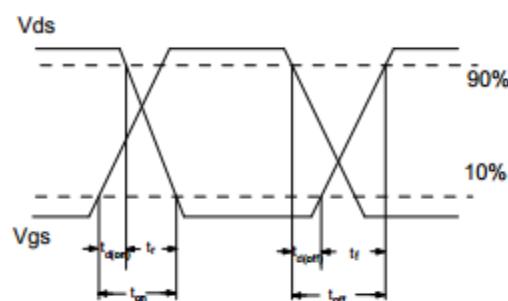
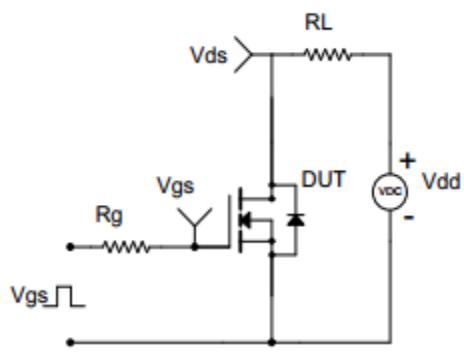


## ■ Test Circuit & Waveform

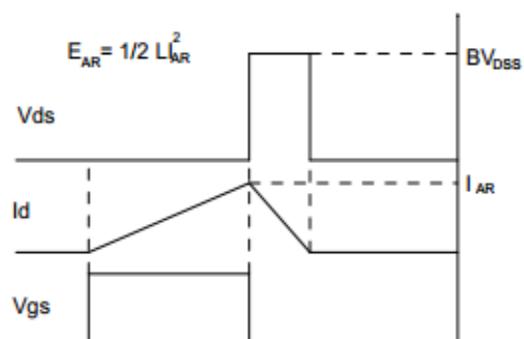
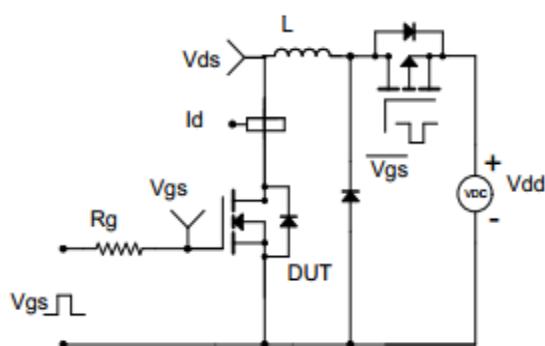
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveform





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**■ TOLL Package Dimensions**

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	2.25	2.3	2.35	e1		1.225	
A1	1.75	1.8	1.85	E	9.85	9.9	9.95
b	0.65	0.7	0.75	E1	8	8.1	8.2
b1	9.75	9.8	9.85	H	11.6	11.7	11.8
b2	0.7	0.75	0.8	H1		6.95	
c	0.45	0.5	0.55	K		3.1	
D	10.35	10.4	10.45	L	1.55	1.65	1.75
D1	11	11.1	11.2	L1	0.65	0.7	0.75
D2	3.25	3.3	3.35	L2	0.5	0.6	0.7
D4	4.5	4.55	4.6	Q		7.95	
e		1.2		θ		10°	

