

MOSFETs Silicon 40V N-Channel MOS
■ Applications

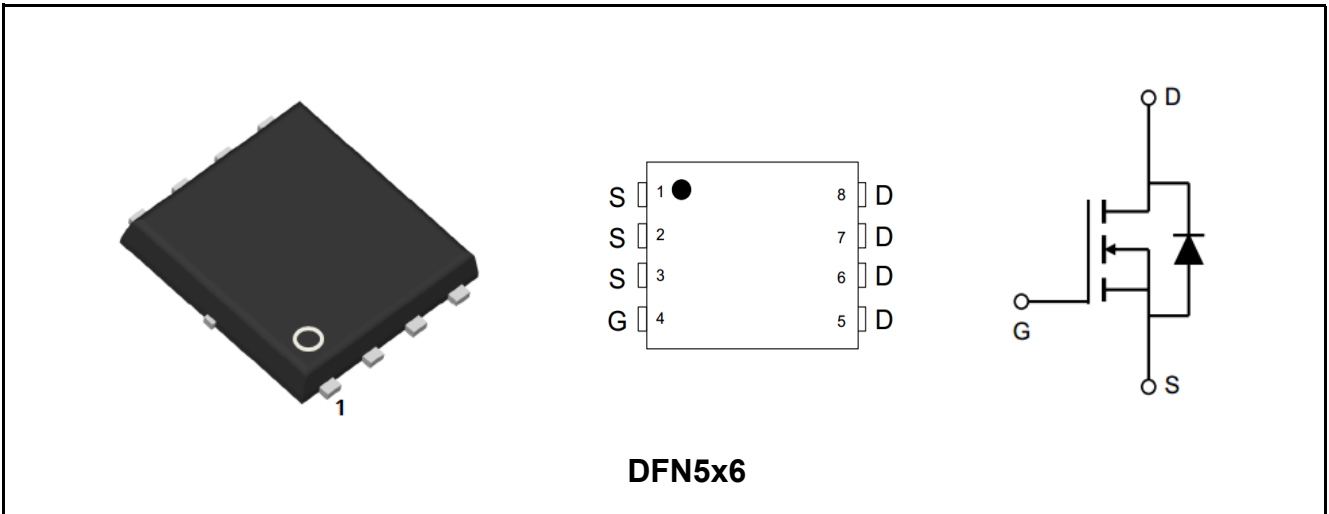
- Synchronous Rectification
- Industrial and Motor Drive
- DC/DC and AC/DC Converters
- Power Tools

■ Features

- High-Speed Switching
- Low $R_{DS(ON)}$
- Low Gate Charge
- Enhanced Avalanche Ruggedness
- RoHS and Halogen-Free Compliant
- 100% UIS and RG Tested

■ Product Summary

V_{DS}	40	V
I_D	110	A
$R_{DS(ON)}, T_{yp}@10V}$	3.4	m Ω
Q_g	21	nC



Marking	Package	Packaging	Min. package quantity
MDG004R040SH	DFN5x6	Tape & Reel	5000





■ Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current Tc=25°C (Note 1)	I_D	110	A
Continuous Drain Current Tc=100°C (Note 1)		70	A
Drain Current-Pulsed (Note 1)	I_{DM}	300	A
Total Dissipation	P_D	83	W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{stg}	-55-150	°C
Single Pulse Avalanche Energy (Note 2)	E_{AS}	220	mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

■ Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	$R_{\theta JC}$	1.5	°C/W
Maximum Junction-to-Ambient (Note 3)	$R_{\theta JA}$	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD}=50V$, $T_{ch}=25^{\circ}C$ (initial), $L=0.5mH$, $R_g=25\Omega$.

Note 3: The value of $R_{\theta JA}$ is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}C$. The value in any given application depends on the user's specific board design.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



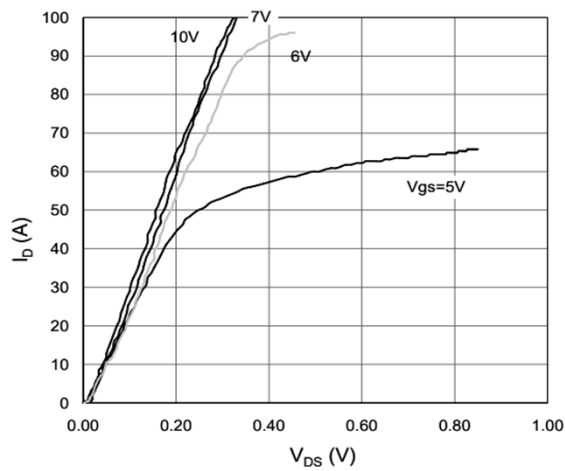
■ Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
Static Parameters							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA		40	-	-	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V		-	-	1	uA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V		-	-	±100	nA
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA		2	2.7	3.5	V
Drain-Source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A		-	3.4	4	mΩ
			Tj=125℃	-	5.2	-	
Dynamic Characteristics							
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f=1.0MHz		-	1400	-	pF
Output Capacitance	C _{oss}			-	477	-	pF
Reverse Transfer Capacitance	C _{rss}			-	44	-	pF
Gate Resistance	R _g	V _{DS} =0V, V _{GS} =0V, f=1.0MHz		-	2.06	-	Ω
Switching Paramters							
Turn-On Delay Time	t _{d(on)}	V _{DS} =20V, I _D =20A, V _{GS} =10V, R _L =2Ω		-	15	-	ns
Turn-On Rise Time	t _r			-	40	-	ns
Turn-Off Delay Time	t _{d(off)}			-	18	-	ns
Turn-Off Fall Time	t _f			-	10	-	ns
Total Gate Charge	Q _g	V _{DS} =20V, I _D =20A, V _{GS} =10V		-	21	-	nC
Gate-Source Charge	Q _{gs}			-	5.9	-	nC
Gate-Drain Charge	Q _{gd}			-	4.8	-	nC
Source-Drain Characteristics							
Diode Forward Voltage	V _{sd}	V _{GS} =0V, I _S =10A		-	0.78	1.2	V
Reverse Recovery Time	t _{rr}	V _R =20V, I _F =20A, di/dt=100A/us		-	38	-	ns
Reverse Recovery Charge	Q _{rr}			-	30	-	nC

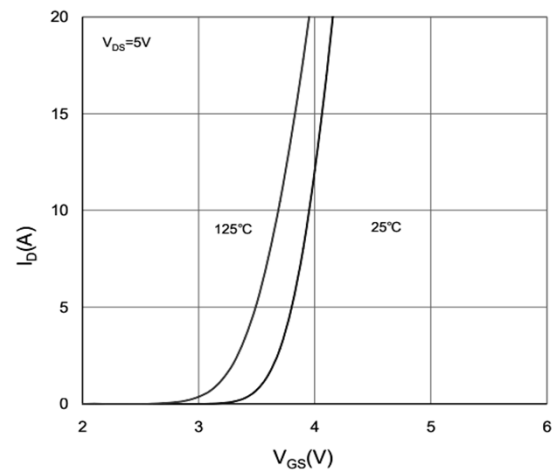




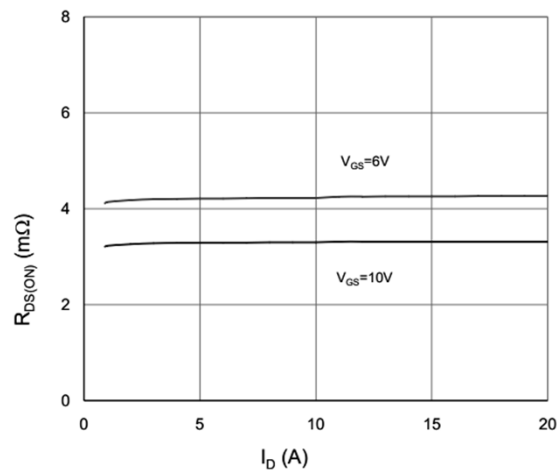
■ Characteristics Curves



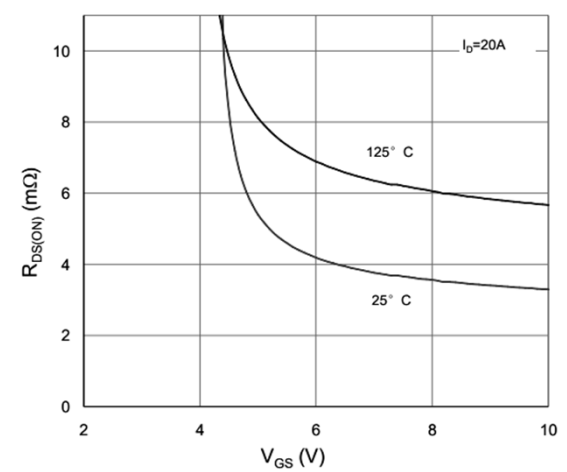
Output Characteristics



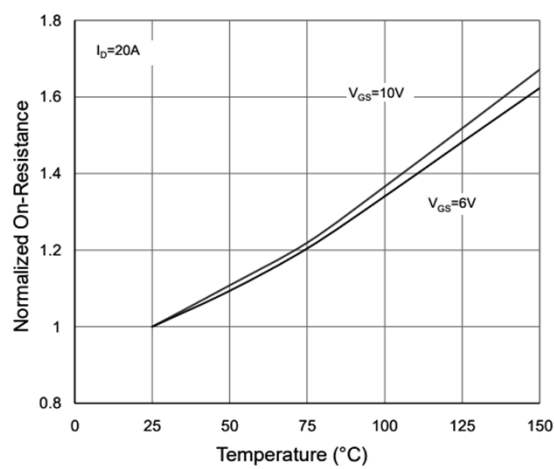
Transfer Characteristics



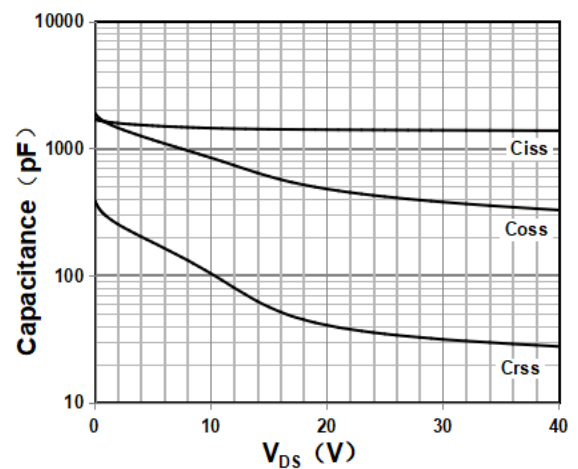
On Resistance Vs Drain Current



On Resistance Vs Gate Source Voltage

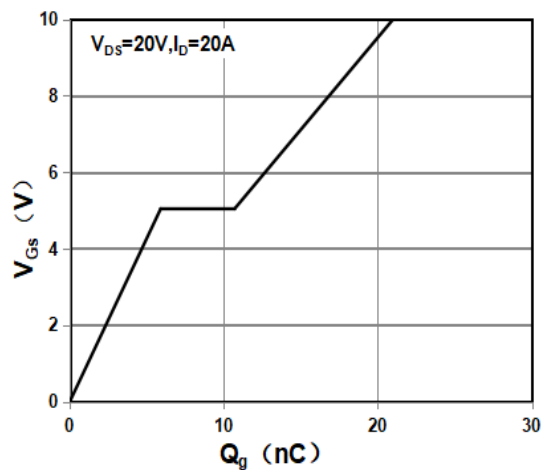


Rdson-JunctionTemperature

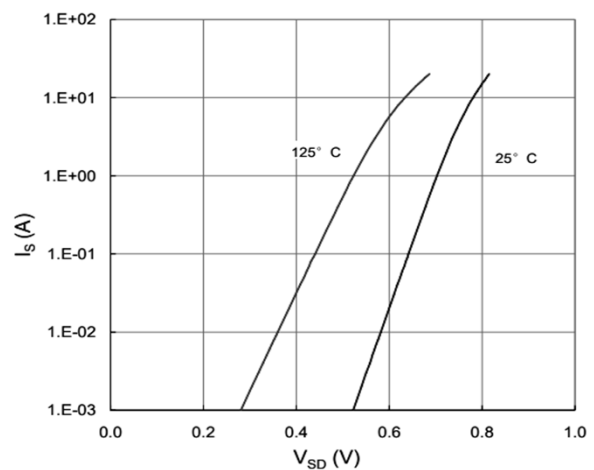


Capacitance

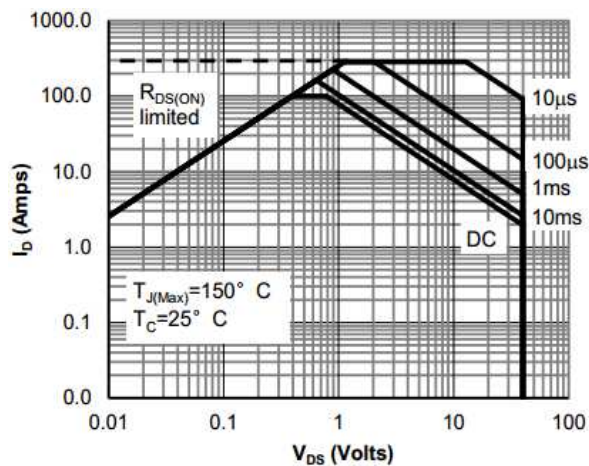




Gate Charge Waveform



Source-Drain Diode Forward Voltage



Maximum Safe Operating Area

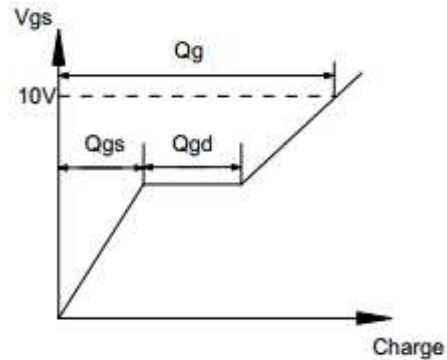
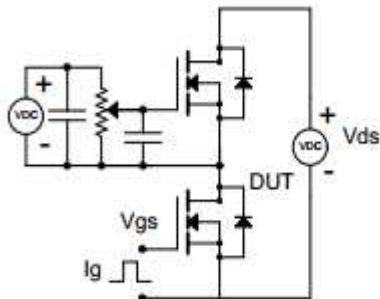
Note : The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



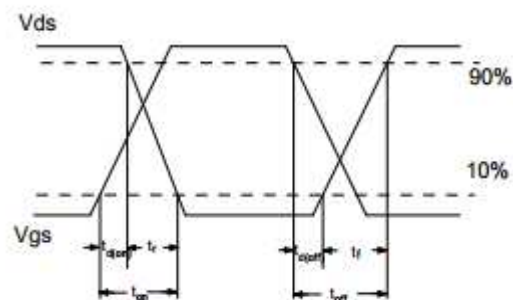
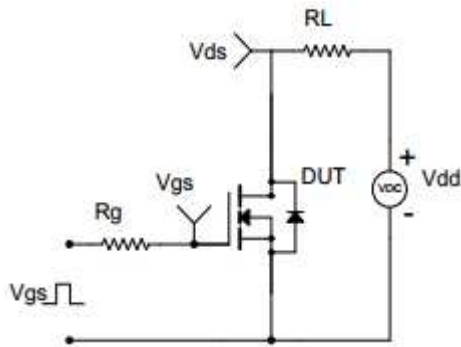


■ Test Circuit & Waveform

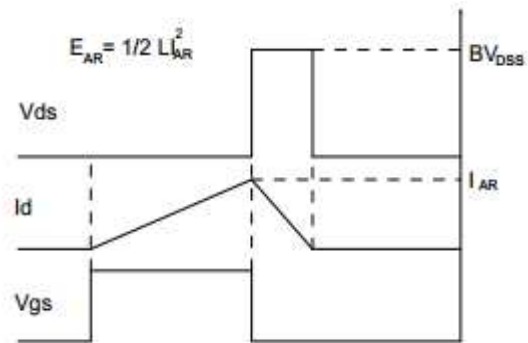
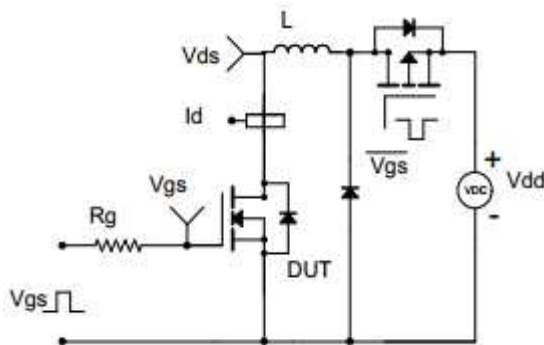
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveform





■ DFN5x6 Package Dimensions

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	0.90		1.10	k	1.15		1.35
A3	0.15		0.30	b	0.20		0.40
D	5.05		5.25	e	1.15		1.35
D1	4		4.2	L	0.50		0.65
D2	4.75		5.05	L1	0.43		0.55
E	5.85		6.15	H	0.55		0.68
E1	3.35		3.55	θ	8°		12°
E2	5.55		5.85				

