

MOSFETs Silicon 600V N-Channel MOS
■ Applications

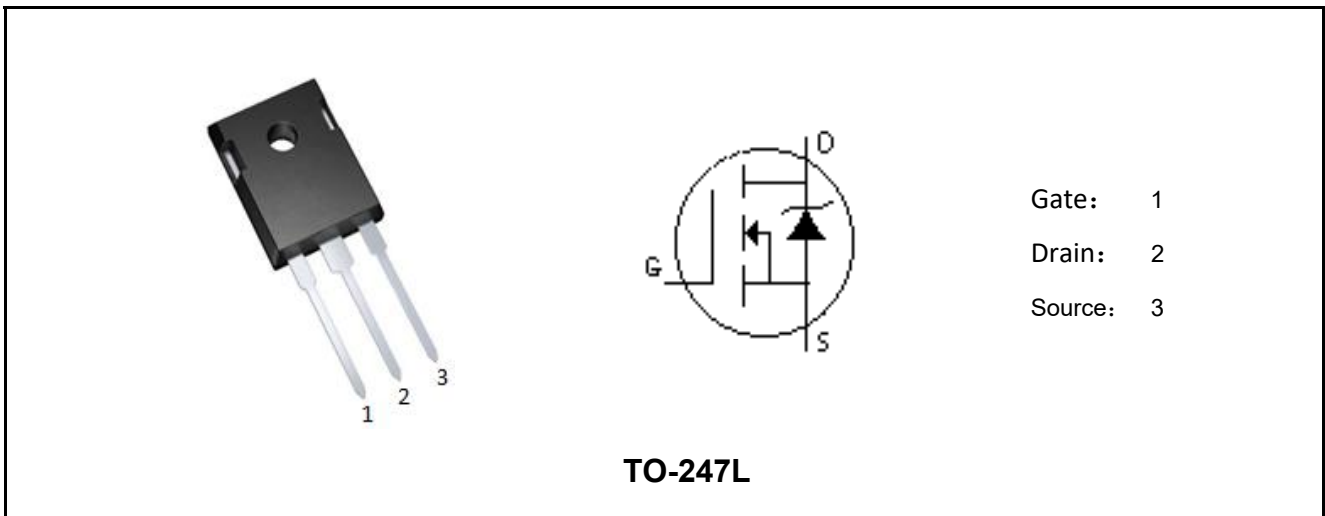
- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- Half bridge or Asymmetric half bridge or Series

■ Features

- Multi-Epi process SJ-FET
- Low $R_{DS(ON)}$
- Ultra Low Gate Charge
- RoHS and Halogen-Free Compliant
- 100% UIS and RG Tested
- Fast-Recovery body diode

■ Product Summary

$V_{DS} @ T_{j,max}$	650	V
I_D	47	A
$R_{DS(ON) ,Typ}@10V$	58	m Ω
Q_g	97	nC



Marking	Package	Packaging	Min. package quantity
MSL070R60CFD2	TO-247L	Tube	450



■ Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	±30	V
Continuous Drain Current Tc=25°C (Note 1)	I_D	47	A
Continuous Drain Current Tc=100°C (Note 1)		29	
Drain Current-Pulsed (Note 1)	I_{DM}	160	A
Total Dissipation	P_D	357	W
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55-150	°C
Single Pulse Avalanche Energy (Note 2)	E_{AS}	1000	mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

■ Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	$R_{\theta JC}$	0.35	°C/W
Maximum Junction-to-Ambient	$R_{\theta JA}$	40	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD}=50V$, $T_{ch}=25^\circ C$ (initial), $L=5mH$, $R_g=25\Omega$.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



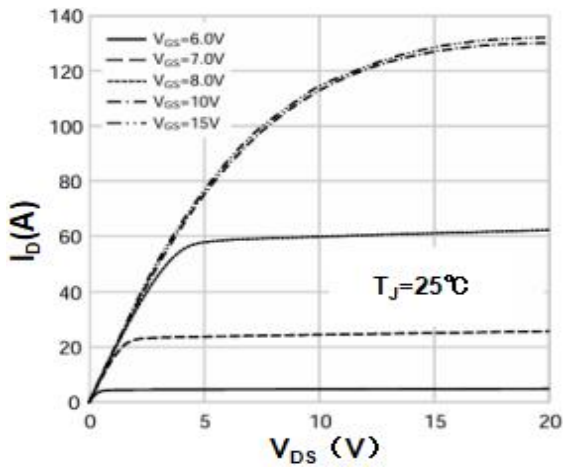
■ Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static Parameters						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	600	-	-	V
		T _j =150°C	650	-	-	
Drain-Source Leakage Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	5	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250μA	3	4	5	V
Drain-Source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =23.5A	-	58	70	mΩ
		T _j =150°C	-	116	-	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=10kHz	-	4205	-	pF
Output Capacitance	C _{oss}		-	4424	-	pF
Reverse Transfer Capacitance	C _{rss}		-	87	-	pF
Gate Resistance	R _g	V _{DS} =0V, V _{GS} =0V, f=1.0MHz	-	1.3	-	Ω
Switching Paramters						
Turn-On Delay Time	t _{d(on)}	V _{DS} =400V, I _D =23.5A, V _{GS} =10V, R _G =5.3Ω	-	38	-	ns
Turn-On Rise Time	t _r		-	49	-	ns
Turn-Off Delay Time	t _{d(off)}		-	85	-	ns
Turn-Off Rise Time	t _f		-	28	-	ns
Total Gate Charge	Q _g	V _{DS} =400V, I _D =23.5A, V _{GS} =10V	-	97	-	nC
Gate-Source Charge	Q _{gs}		-	33	-	nC
Gate-Drain Charge	Q _{gd}		-	43	-	nC
Source-Drain Characteristics						
Max. Diode Forward Current	I _S		-	-	47	A
Max. Pulsed Forward Current	I _{SM}		-	-	160	A
Diode Forward Voltage	V _{sd}	V _{GS} =0V, I _S =23.5A	-	0.8	-	V
Reverse Recovery Time	t _{rr}	V _R =400V, I _F =23.5A, di/dt=100A/μs	-	131	-	ns
Reverse Recovery Charge	Q _{rr}		-	0.8	-	μC

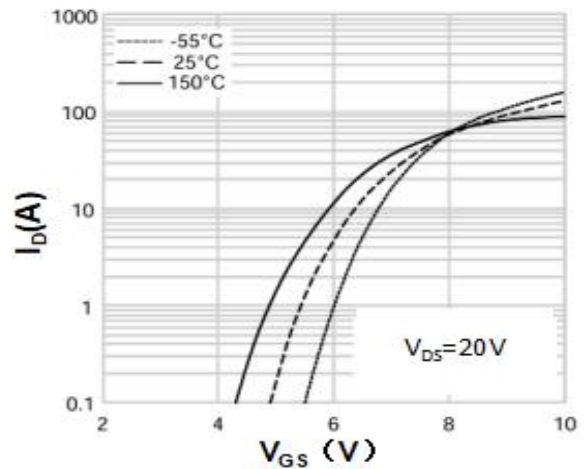




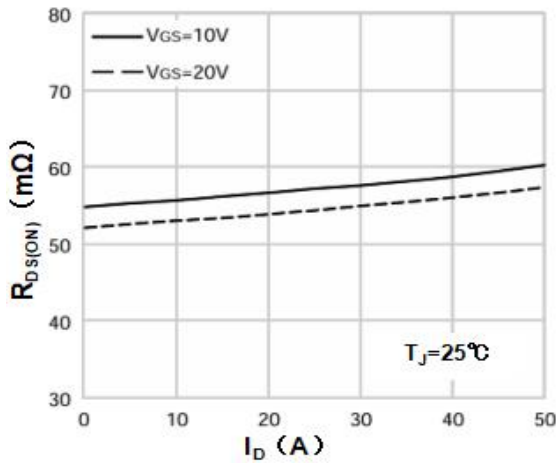
■ Characteristics Curves



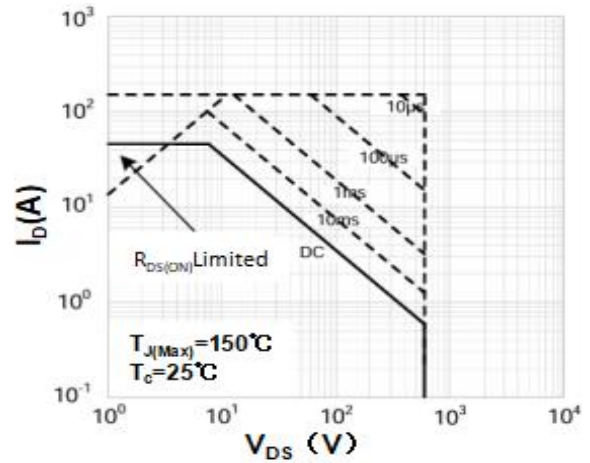
Output Characteristics



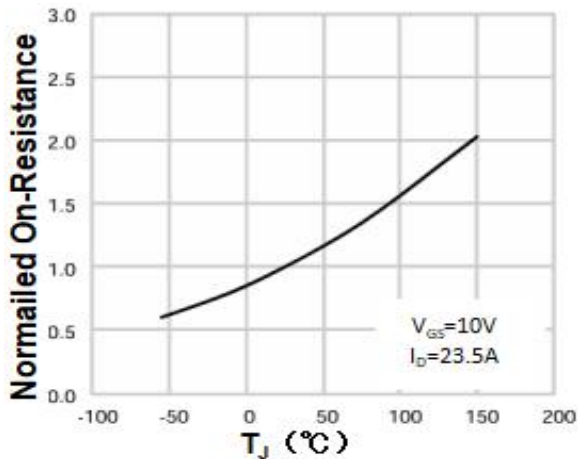
Transfer Characteristics



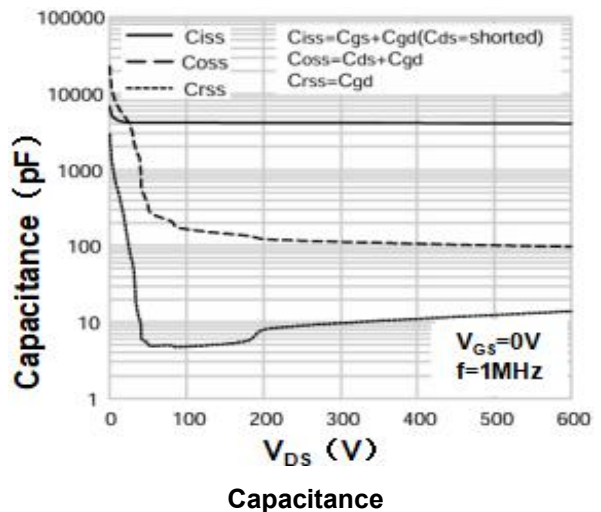
On Resistance Vs Drain Current



Maximum Safe Operating Area

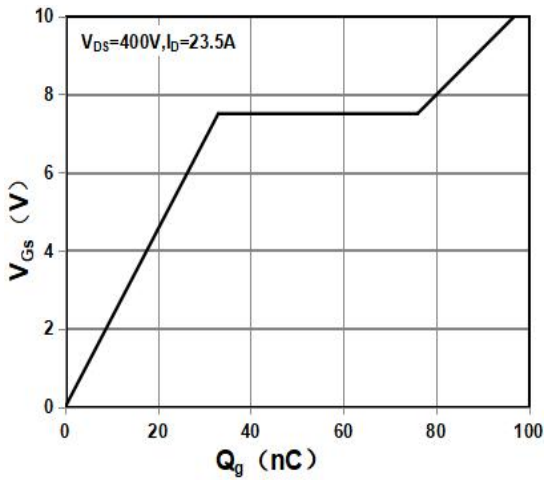


Rdson-Junction Temperature

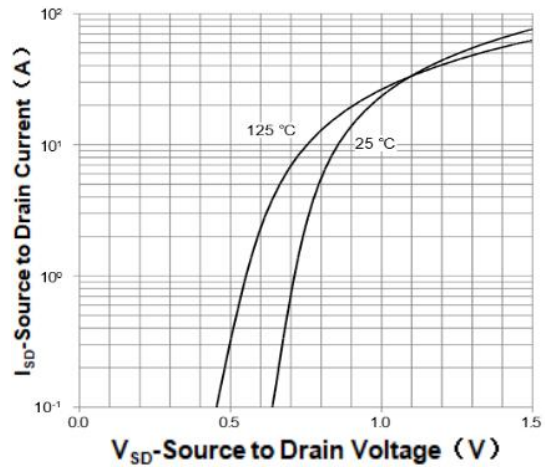


Capacitance

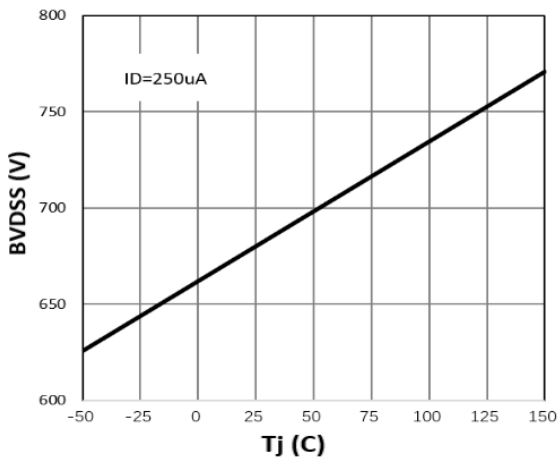




Gate Charge Waveform



Source-Drain Diode Forward Voltage



Breakdown Voltage Vs Junction Temperature

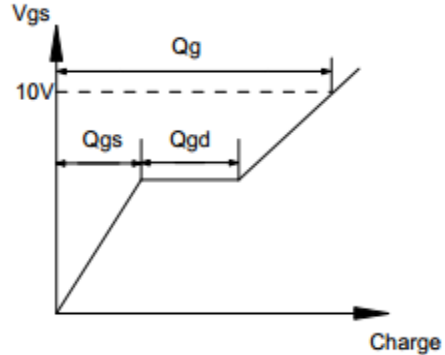
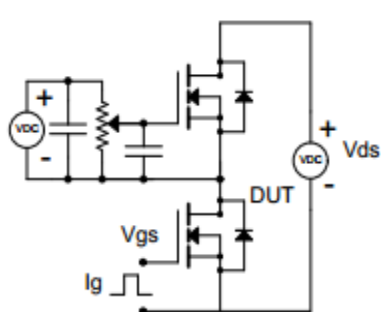
Note : The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



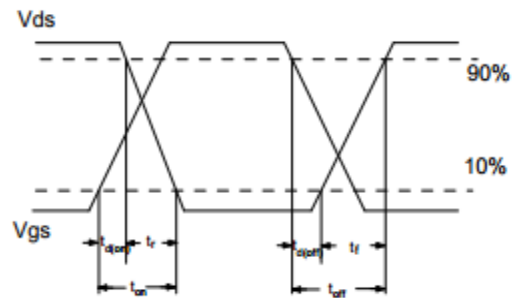
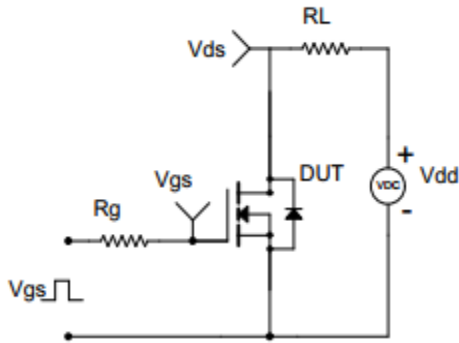


■ Test Circuit & Waveform

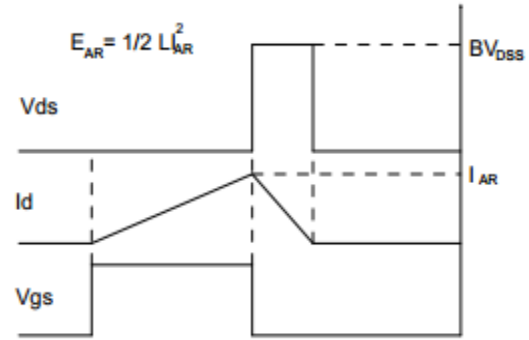
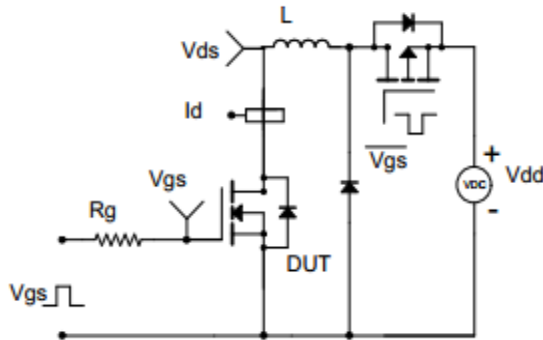
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveform





■ TO-247L Package Dimensions

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	4.80		5.20	E1	13.00		13.60
A1	2.20	2.40	2.60	E2	5.00		5.50
A2	1.85		2.15	E3	1.90		2.60
b	1.07		1.33	e		5.44	
b2	1.90		2.16	L	19.30		19.90
b4	2.90		3.20	L1	3.75	3.95	4.15
c	0.52		0.68	ΦP	3.40		3.80
D	20.70		21.30	ΦP1	7.00		7.40
D1	16.15		16.95	S	6.04	6.15	6.30
E	15.50		16.10				

