

Silicon Carbide Power MOSFET 650V N-Channel MOS

■ Applications

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- Battery Chargers
- Motor Drives
- Pulsed Power applications

■ Features

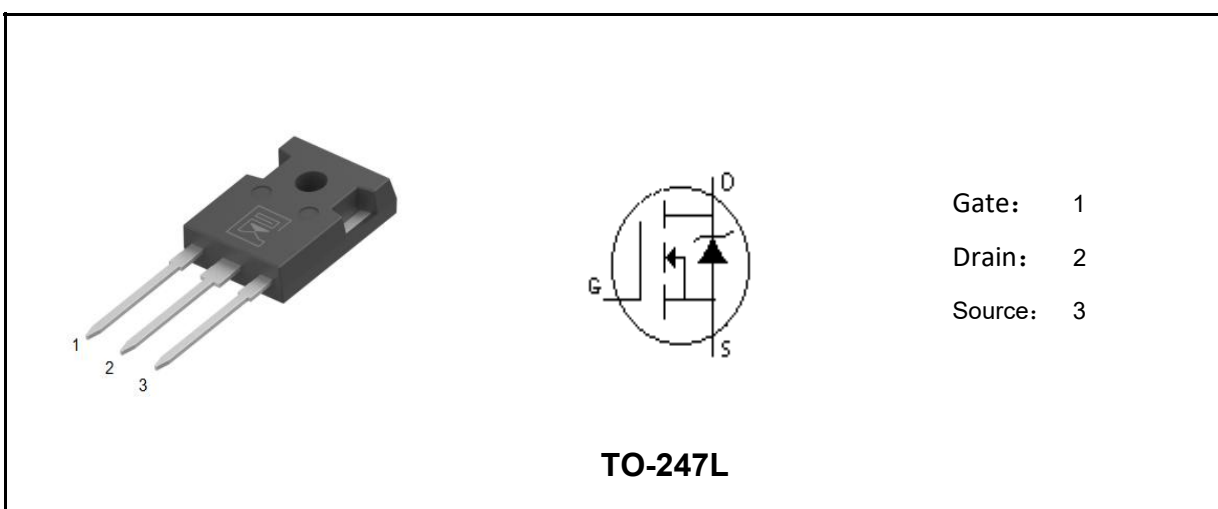
- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness
- Halogen Free, RoHS Compliant

■ Benefits

- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

■ Product Summary

V_{DS}	650	V
I_D	35	A
$R_{DS(ON)}$, Typ@18V	50	mΩ
Q_g	46	nC



Marking	Package	Packaging	Min. package quantity
MSL3C050R065	TO-247L	Tube	450



**■ Absolute Maximum Ratings (Tc=25°C unless otherwise noted)**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	- 10/+20	V
Recommended operational values	V_{GSop}	- 4/+18	V
Continuous Drain Current Tc=25°C V_{GS} =18V (Note 1)	I_D	35	A
Continuous Drain Current Tc=100°C V_{GS} =18V (Note 1)		25	A
Drain Current-Pulsed (Note 1)	I_{DM}	96	A
Total Dissipation	P_D	150	W
Junction Temperature	T_j	175	°C
Storage Temperature	T_{stg}	- 55~175	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

■ Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	$R_{\theta JC}$	1	°C/W
Maximum Junction-to-Ambient	$R_{\theta JA}$	40	°C/W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.





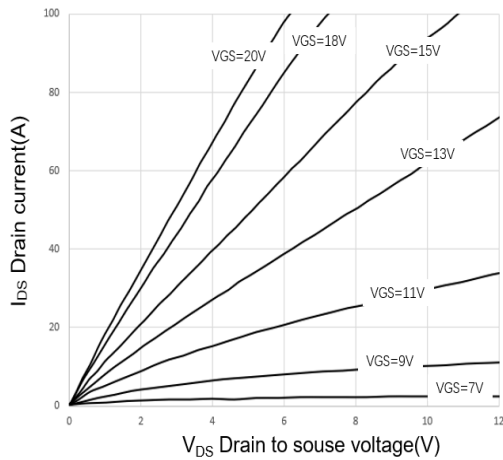
■ Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static Parameters						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =100uA	650	-	-	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V	-	-	100	uA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =20V, V _{DS} =0V	-	-	250	nA
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _{DS} =5mA	1.5	2.6	3.5	V
Drain-Source On Resistance	R _{DS(ON)}	V _{GS} =15V, I _D =20A	-	68	-	mΩ
	R _{DS(ON)}	V _{GS} =18V, I _D =20A	-	50	67	
		T _j =150℃	-	56	75	
Transconductance	g _{fs}	V _{GS} =20V, I _D =20A	-	4		S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	953	-	pF
Output Capacitance	C _{oss}		-	414	-	pF
Reverse Transfer Capacitance	C _{rss}		-	38	-	pF
Gate Resistance	R _g	V _{DS} =0V, V _{GS} =0V, f=1.0MHz	-	1.6	-	Ω
Switching Paramters						
Turn-On Delay Time	t _{d(on)}	V _{DD} =400V, V _{GS} =-4/18 V I _D = 15.4A, R _{G(ext)} =2. 5 Ω , L=59uH	-	10	-	ns
Turn-On Rise Time	t _r		-	45	-	ns
Turn-Off Delay Time	t _{d(off)}		-	26	-	ns
Turn-Off Fall Time	t _f		-	24	-	ns
Turn-On Switching Energy	E _{ON}		-	330	-	uJ
Turn-Off Switching Energy	E _{OFF}		-	20	-	
Total Gate Charge	Q _g	V _{DD} =400V, V _{GS} =-4/18 V I _D = 15.4A	-	46	-	nC
Gate-Source Charge	Q _{gs}		-	11.5	-	nC
Gate-Drain Charge	Q _{gd}		-	15	-	nC
Source-Drain Characteristics						
Diode Forward Voltage	V _{sd}	V _{GS} =-4V, I _{SD} = 10 A,	-	3.7	-	V
Continuous Diode Forward Current	I _S	T _C =25℃	-	-	43	A
Reverse Recovery Time	t _{rr}	VR=400V, IF=30A, di/dt=200A/us	-	32	-	ns
Reverse Recovery Charge	Q _{rr}		-	61	-	nC
Peak Reverse Recovey Current	I _{mm}		-	3.8	-	A

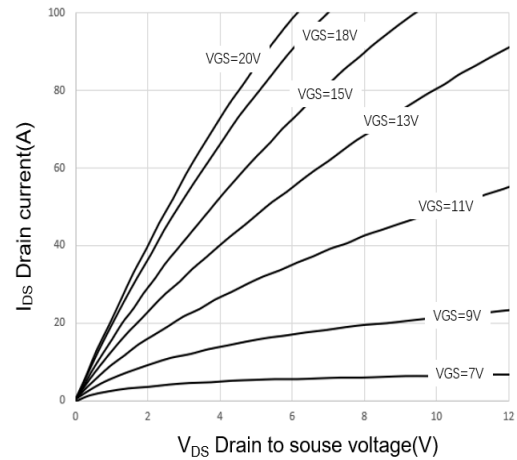




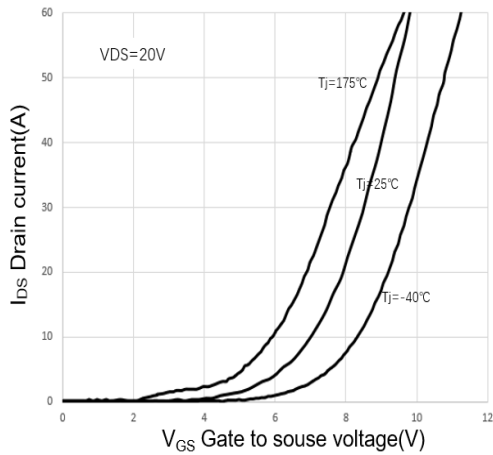
■ Characteristics Curves



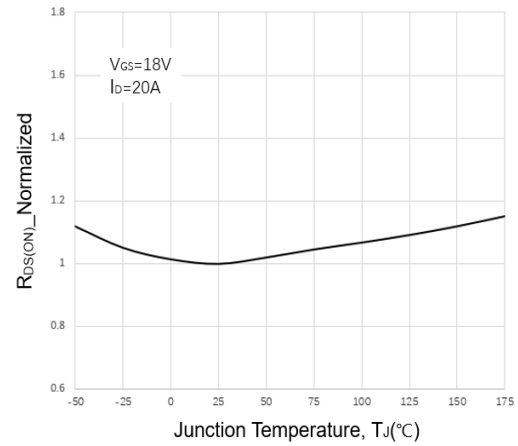
Output Characteristics $T_J = -40^\circ\text{C}$



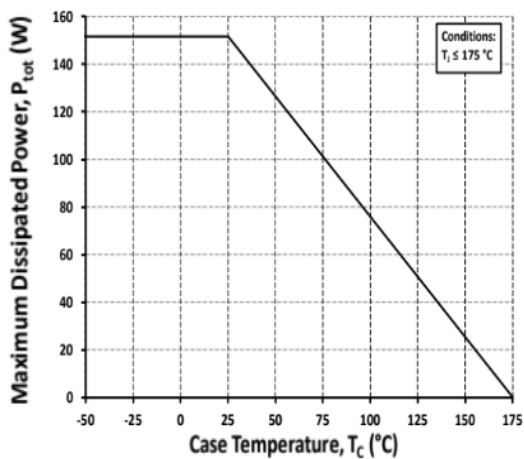
Output Characteristics $T_J = 25^\circ\text{C}$



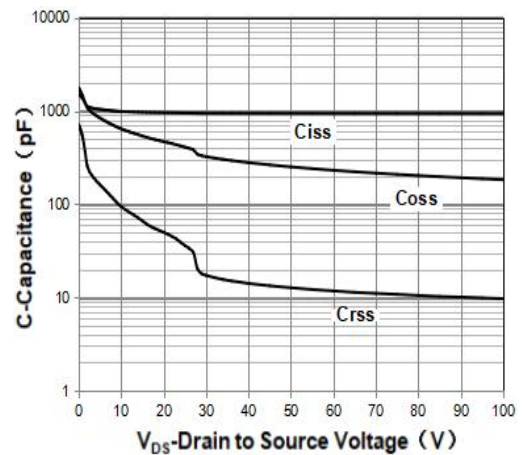
Transfer Characteristics



Normalized On-Resistance vs. Temperature

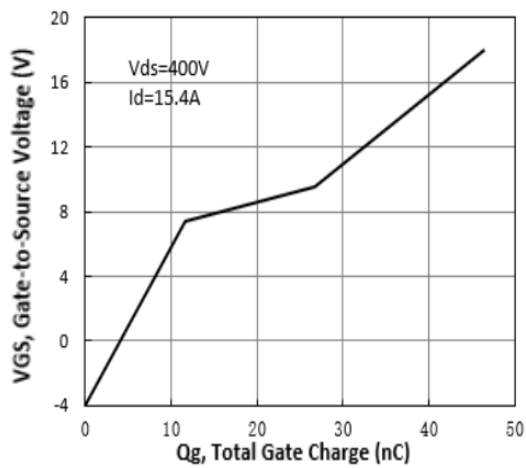


Power Dissipation Derating

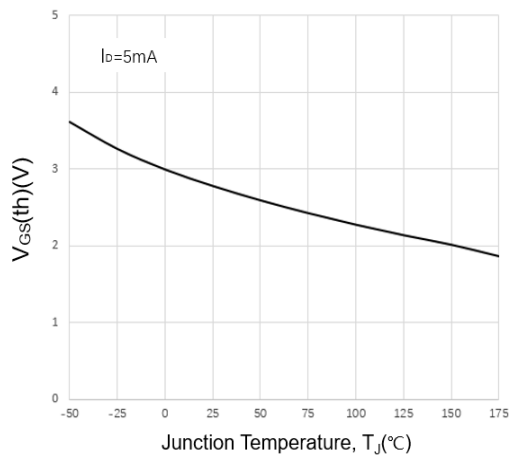


Capacitance

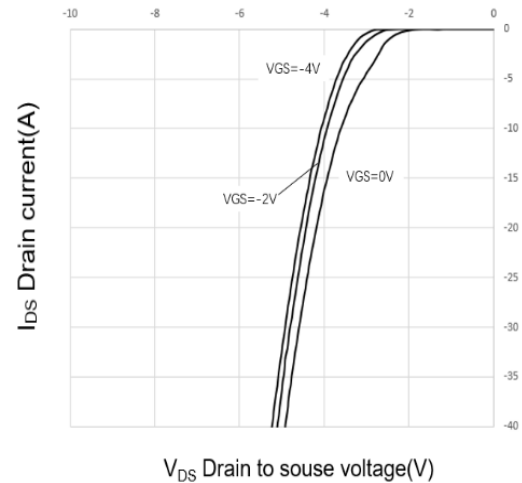




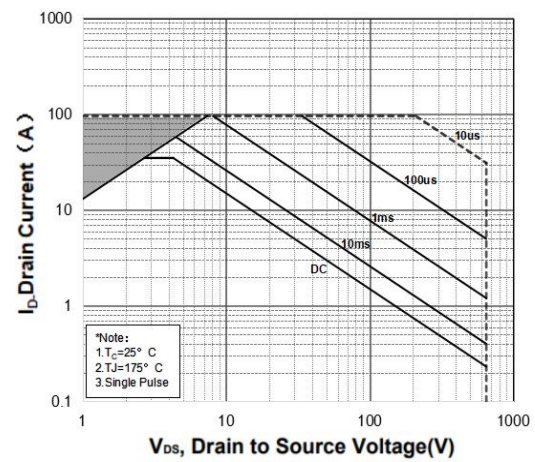
Gate Charge Waveform



Threshold Voltage vs. Temperature



Source-Drain Diode Characteristics, $T_J=25^{\circ}\text{C}$



Safe Operating Area

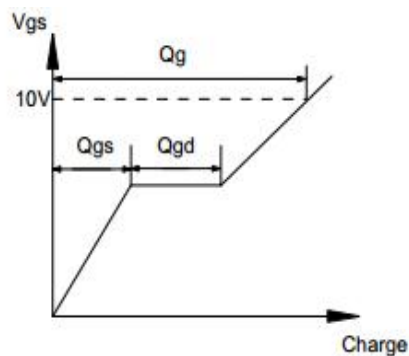
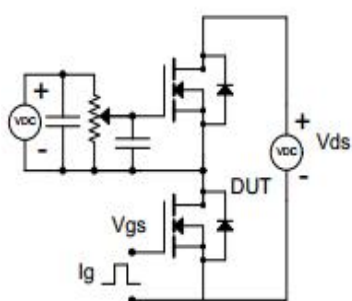
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



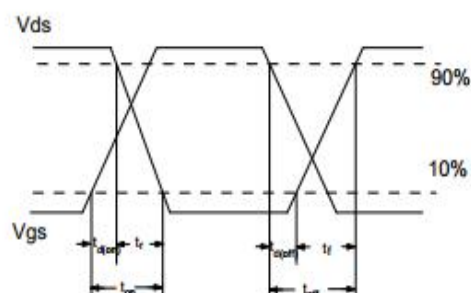
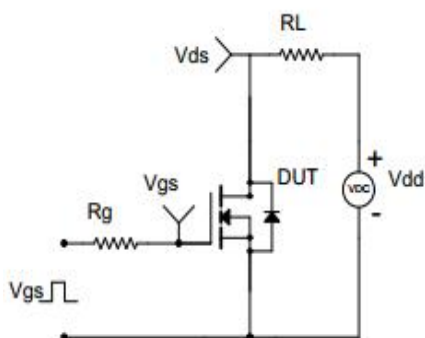


■ Test Circuit & Waveform

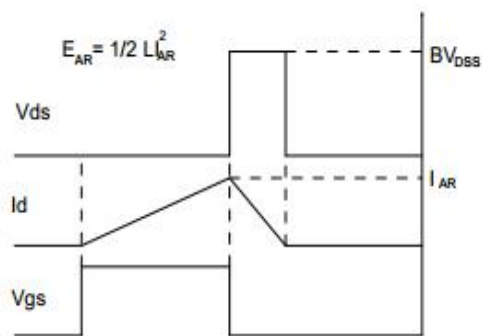
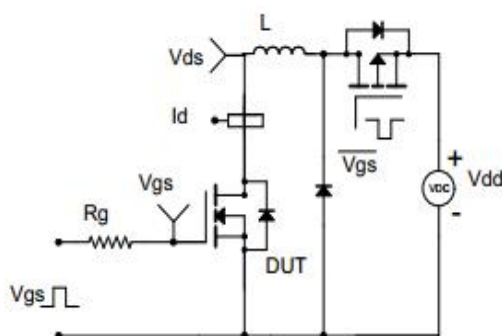
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveform



**■ TO-247L Package Dimensions**

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	4.80		5.20	E1	13.00		13.60
A1	2.20	2.40	2.60	E2	5.00		5.50
A2	1.85		2.15	E3	1.90		2.60
b	1.07		1.33	e		5.44	
b2	1.90		2.16	L	19.30		19.90
b4	2.90		3.20	L1	3.95	4.15	4.35
c	0.52		0.68	ΦP	3.40		3.80
D	20.70	20.80	21.30	ΦP1	7.00		7.40
D1	16.15		16.95	S	6.04	6.15	6.30
E	15.50	15.60	16.10				

