

» Applications

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- EV Charger

» Product Summary

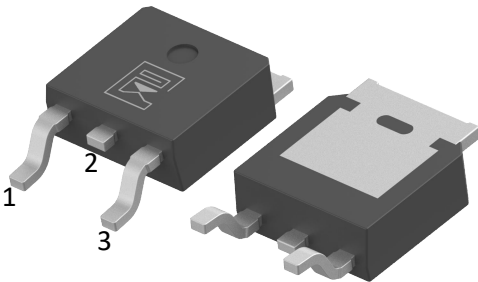
$V_{DS} @ T_{j,max}$	550	V
I_D	20	A
$R_{DS(ON), Typ@10V}$	0.17	Ω
Q_g	22	nC

» Features

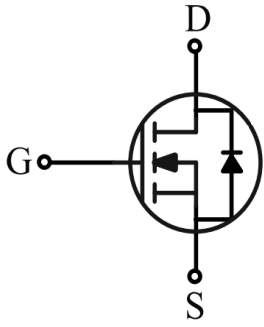
- Multi-Epi process SJ-FET
- Low $R_{DS(ON)}$
- Ultra Low Gate Charge
- 100% UIS and RG Tested
- Extremely low switching loss



TO-252



1
2
3



D
G
S

Gate: 1
Drain: 2
Source: 3

» Package Marking and Ordering Information

Ordering code	Marking	Package	Packaging	Min. package quantity
MK190R50C2	MK190R50C2	TO-252	Tape & Reel	3000

»» Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	±30	V
Continuous Drain Current Tc=25°C (Note 1)	I_D	20	A
Continuous Drain Current Tc=100°C (Note 1)		13	
Drain Current-Pulsed (Note 1)	I_{DM}	65	A
Total Dissipation	P_D	90	W
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55-150	°C
Single Pulse Avalanche Energy (Note 2)	E_{AS}	46	mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

»» Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	$R_{\theta JC}$	1.4	°C/W
Maximum Junction-to-Ambient	$R_{\theta JA}$	60	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD}=100V$, $T_{ch}=25^\circ C$ (initial), $L=0.5mH$, $R_g=25\Omega$.

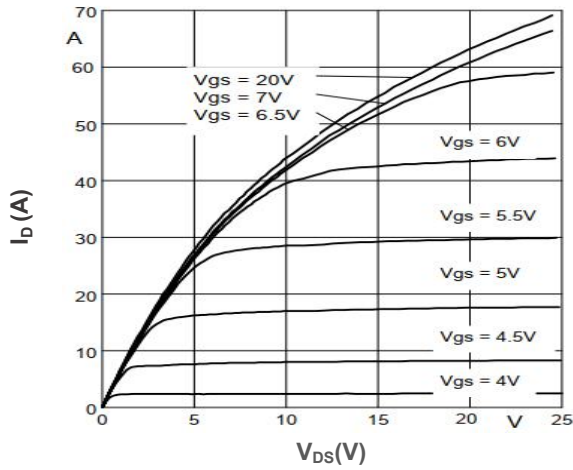
Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

Electrical Characteristics (Tc=25°C unless otherwise noted)

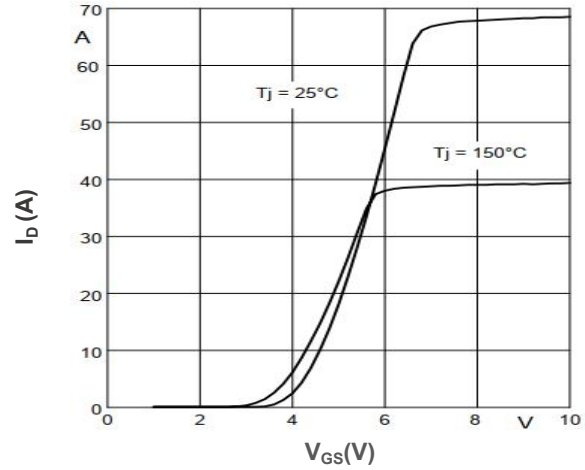
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static Parameters						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	500	-	-	V
		T _j =150°C	550	-	-	
Drain-Source Leakage Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250μA	2.1	3.1	4.1	V
Drain-Source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =10A	-	0.17	0.19	Ω
		T _j =150°C	-	0.40	-	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	1030	-	pF
Output Capacitance	C _{oss}		-	1000	-	pF
Reverse Transfer Capacitance	C _{rss}		-	25	-	pF
Gate Resistance	R _g	V _{DS} =0V, V _{GS} =0V, f=1.0MHz	-	2.5	-	Ω
Switching Paramters						
Turn-On Delay Time	t _{d(on)}	V _{DS} =400V, I _D =10A, V _{GS} =10V, R _G =2Ω	-	20	-	ns
Turn-On Rise Time	t _r		-	7	-	ns
Turn-Off Delay Time	t _{d(off)}		-	35	-	ns
Turn-Off Fall Time	t _f		-	4	-	ns
Total Gate Charge	Q _g	V _{DS} =400V, I _D =20A, V _{GS} =10V	-	22	-	nC
Gate-Source Charge	Q _{gs}		-	8.6	-	nC
Gate-Drain Charge	Q _{gd}		-	4.6	-	nC
Source-Drain Characteristics						
Max. Diode Forward Current	I _S		-	-	20	A
Max. Pulsed Forward Current	I _{SM}		-	-	65	A
Diode Forward Voltage	V _{sd}	V _{GS} =0V, I _S =10A	-	0.85	1.2	V
Reverse Recovery Time	t _{rr}	V _R =300V, I _F =10A, di/dt=100A/μs	-	197	-	ns
Reverse Recovery Charge	Q _{rr}		-	1.92	-	μC



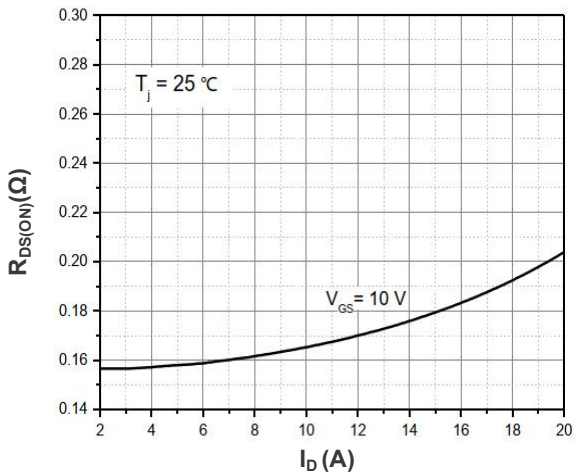
Characteristics Curves



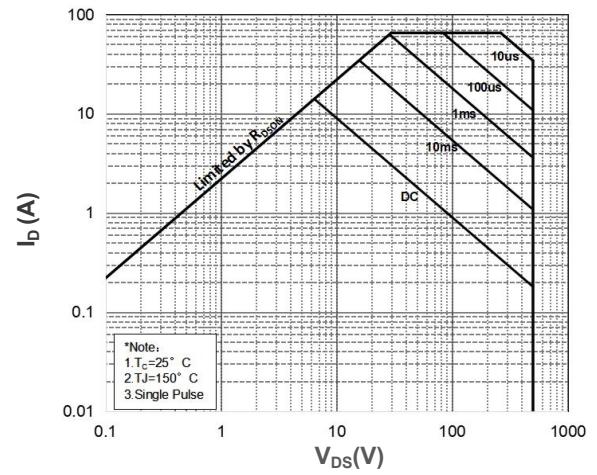
Output Characteristics



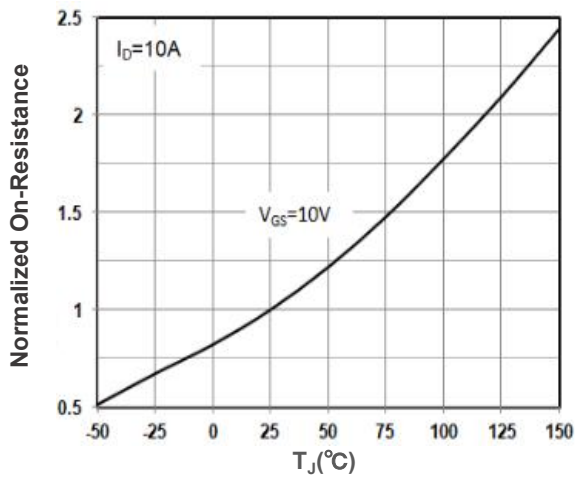
Transfer Characteristics



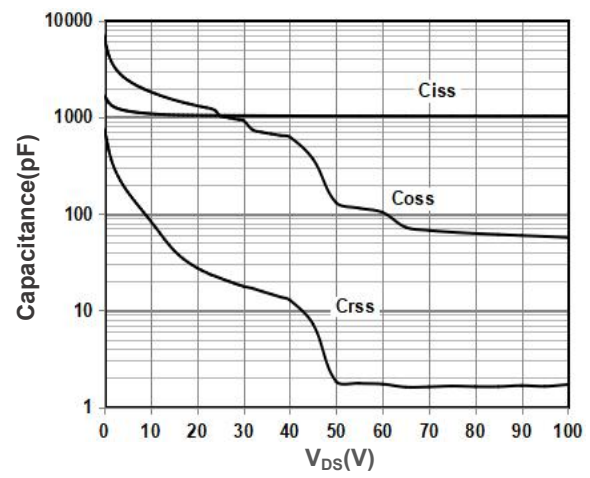
On Resistance Vs Drain Current



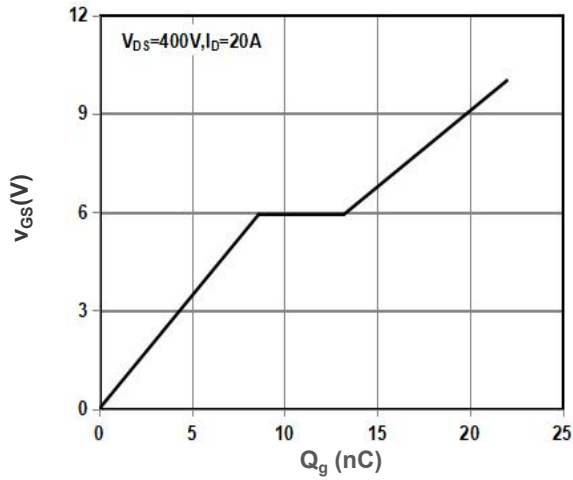
Maximum Safe Operating Area



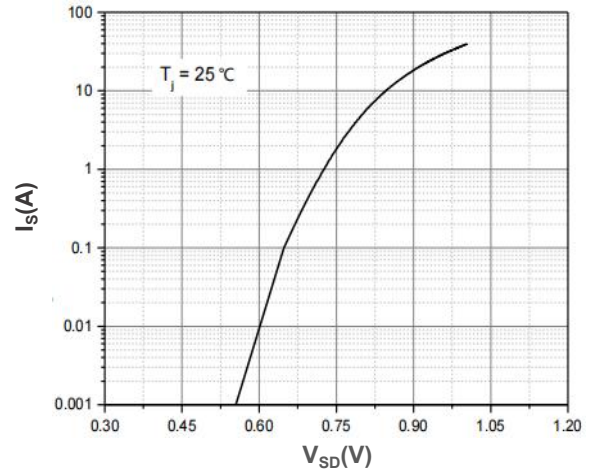
Rdson-Junction Temperature



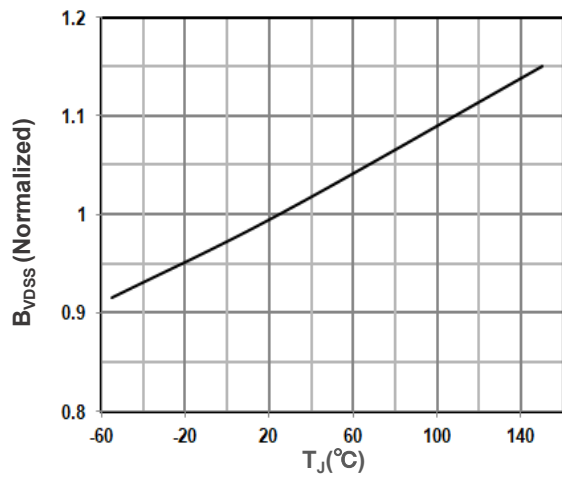
Capacitance



Gate Charge Waveform



Source-Drain Diode Forward Voltage



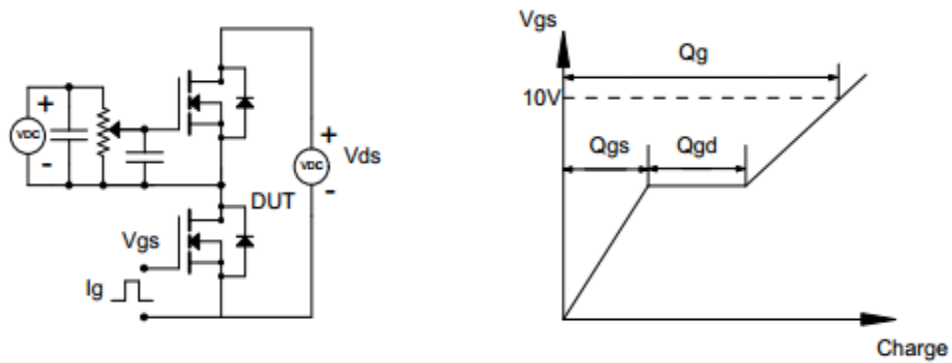
Breakdown Voltage Vs Junction Temperature

Note : The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

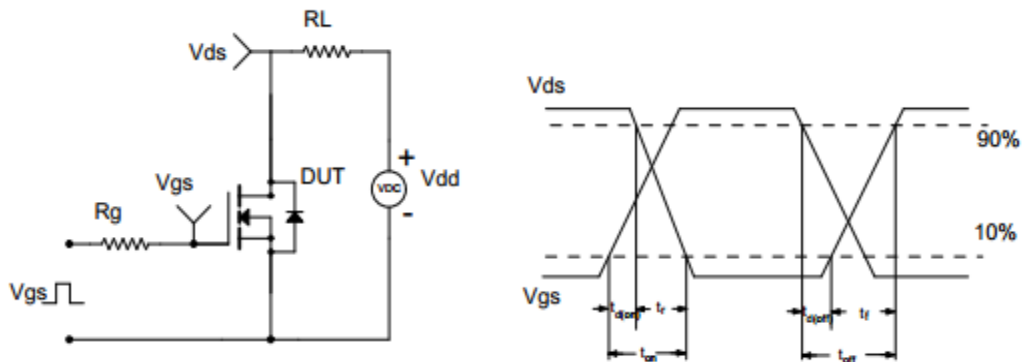


Test Circuit & Waveform

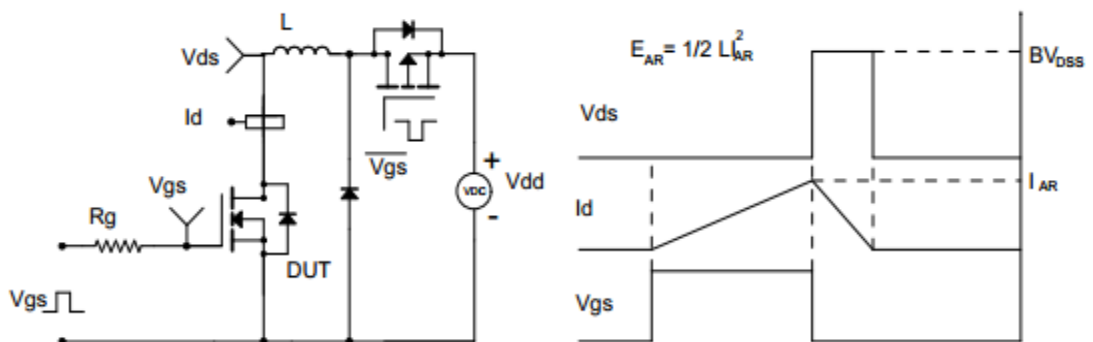
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveform





TO-252 Package Dimensions

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	2.20		2.40	L	9.80		10.40
A1	0.00		0.13	L1		2.90	
b	0.66		0.86	L2	1.40		1.60
c	0.46		0.58	L3		1.80	
D	6.50		6.70	L4	0.60		1.00
D1	5.10		5.46	L5	0.90		1.25
D2		4.83		Φ	1.1		1.30
E	6.00		6.20	θ	0°		8°
e	2.19		2.39	V		5.35	

